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DECLARATION AND POWER OF ATTORNEY
FOR PATENT APPLICATIONS

PATENT

Docket No.: 37115/JFO/B600

As a below named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below next to my name.

I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled ASYNCHRONOUSLY-RESETTABLE DECODER WITH REDUNDANCY, the application of which is attached hereto unless the following is checked:

X was filed on February 2, 2001 as United States Application Number or PCT International Application Number 09/775,476 and was amended on (if applicable).

I hereby state that I have reviewed and understand the contents of the above-identified specification, including the claims, as amended by any amendment referred to above.

I acknowledge the duty to disclose information which is material to patentability as defined in 37 CFR § 1.56.

I hereby claim foreign priority benefits under 35 U.S.C. § 119(a)-(d) or § 365(b) of the foreign application(s) for patent or inventor's certificate, or § 365(a) of any PCT International application which designated at least one country other than the United States, listed below and have also identified below, any foreign application for patent or inventor's certificate, or PCT International application having a filing date before that of the application on which priority is claimed.

Prior Foreign Application(s)

| <u>Application Number</u> | <u>Country</u> | <u>Filing Date (day/month/year)</u> | <u>Priority Claimed</u> |
|---------------------------|----------------|-------------------------------------|-------------------------|
|---------------------------|----------------|-------------------------------------|-------------------------|

I hereby claim the benefit under 35 U.S.C. § 119(e) of any United States provisional application(s) listed below.

| <u>Application Number</u> | <u>Filing Date</u> |
|---------------------------|--------------------|
|---------------------------|--------------------|

| | |
|------------|------------------|
| 60/179,777 | February 2, 2000 |
| 60/179,766 | February 2, 2000 |
| 60/179,866 | February 2, 2000 |
| 60/179,718 | February 2, 2000 |
| 60/179,765 | February 2, 2000 |
| 60/179,768 | February 2, 2000 |
| 60/179,865 | February 2, 2000 |
| 60/193,607 | March 31, 2000 |
| 60/193,606 | March 31, 2000 |
| 60/193,605 | March 31, 2000 |
| 60/215,741 | June 29, 2000 |
| 60/220,567 | June 25, 2000 |

I hereby claim the benefit under 35 U.S.C. § 120 of any United States application(s), or any PCT International application designating the United States, listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States or PCT International application in the manner provided by the first paragraph of 35 U.S.C. § 112, I acknowledge the duty to disclose information which is

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material to patentability as defined in 37 CFR § 1.56 which became available between the filing date of the prior application and the national or PCT International filing date of this application:

| <u>Title</u> | <u>Docket No.</u> | <u>Filing Date</u> | <u>Patented/Pending/ Abandoned</u> |
|---|-------------------|--------------------|--|
| MEMORY MODULE WITH HIERARCHICAL FUNCTIONALITY | B600:40050 | February 2, 2001 | Pending |
| A CIRCUIT TECHNIQUE FOR HIGH SPEED LOW POWER DATA | B600:37497 | February 2, 2001 | Pending |
| SINGLE-ENDED SENSE AMPLIFIER WITH SAMPLE- AND-HOLD REFERENCE | B600:37362 | February 2, 2001 | Pending |
| LIMITED SWITCH DRIVER CIRCUIT | B600:37361 | February 2, 2001 | Pending |
| HIGH PRECISION DELAY MEASUREMENT CIRCUIT | B600:37079 | February 2, 2001 | Pending |
| DIFFUSION REPLICA DELAY CIRCUIT | B600:37360 | February 2, 2001 | Pending |
| SENSE AMPLIFIER WITH OFFSET CANCELLATION AND | B600:37363 | February 2, 2001 | Pending |
| MEMORY ARCHITECTURE WITH SINGLE-PORT CELL AND DUAL-PORT (READ AND WRITE) FUNCTIONALITY | B600:37364 | February 2, 2001 | Pending |
| MEMORY REDUNDANCY IMPLEMENTATION | B600:37496 | February 2, 2001 | Pending |

POWER OF ATTORNEY: I hereby appoint the following attorneys and agents of the law firm CHRISTIE, PARKER & HALE, LLP to prosecute this application and any international application under the Patent Cooperation Treaty based on it and to transact all business in the U.S. Patent and Trademark Office connected with either of them in accordance with instructions from the assignee of the entire interest in this application; or from the first or sole inventor named below in the event the application is not assigned; or from __ in the event the power granted herein is for an application filed on behalf of a foreign attorney or agent.

| | | | | | |
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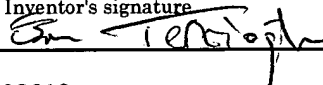
Docket No. 37115/JFO/B600

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The authority under this Power of Attorney of each person named above shall automatically terminate and be revoked upon such person ceasing to be a member or associate of or of counsel to that law firm.

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I declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

| | | |
|---|--|-----------------------------|
| Full name of sole or first joint inventor Esin Terzioglu | Inventor's signature  | Date 3/21/2001 |
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| | | |
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| Full name of second joint inventor Morteza Cyrus Afghahi | Inventor's signature M. Afghahi | Date 2/17/2001 |
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